

500.40530X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yasuo OSONE, et al
Filed: August 31, 2001
For: MULTILAYER WIRING BOARD AND SEMICONDUCTOR
DEVICE



CLAIM FOR PRIORITY

Assistant Commissioner for Patents
Washington, D.C. 20231

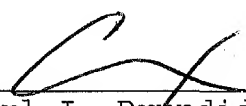
August 31, 2001

Sir:

Under the provisions of 35 USC §119 AND 37 CFR § 1.55,
Applicants hereby claim the right of priority based on Patent
Application No. 2001-150260 filed in Japan on May 21, 2001.

Respectfully submitted,

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